VASANTRAO NAIK MAHAVIDYALAYA
AIRPORT ROAD, CIDCO, AURANGABAD
-Affiliated to Dr. Babasaheb Ambedkar Marathwada University, Aurangabad. (M.S.) India, *NAAC Accredited - B + grade

President
Rajaramji Rathod

Secretary Nitinji Rathod

I/C Principal Dr. Jagdish Bharad

## Department of Computer Science

## Year 2020-2021

Department of Computer Science was set up in the year June 1997. Under Graduate programs are run by the Dept. These include B.Sc. Computer Science (Optional), Bachelor of Computer Application (B.C.A.), B.Sc. Computer Science (B.C.S.), Compulsory Computer course for B.A and B.Sc.

Students admitted to under graduate courses come from socially and economically weaker sections of the society. They are found average and need special efforts to come up to the expectations of the industry and business. These students are particularly weak in Communication Skills.

There are well furnished Two software laboratories and 1 hardware laboratory with latest configuration given below:

## Computer Lab C1 - (Total No. of PC-22)

Computer Configuration
Hard Disk 80GB, RAM 2GB, Motherboard Intel \& AMD ,Processor (Intel 2.6 GHz ), Monitor TFT 15 inch, Mouse (USB), Keyboard(USB), Dot matrix printer (LQ-300+EPSON-one), Switch D link 24 port (1).

## Computer Lab C2 - (Total No. of PC-24)

Computer Configuration
Hard Disk 1 TB and 500 GB , RAM $4 \mathrm{~GB} \& 8 \mathrm{~GB}$, Motherboard Intel \& ASUS, Processor (Intel I 3), Monitor LED \& TFT 15 inch, Mouse (USB), Keyboard(USB), Dot matrix printer (LQ-300+EPSON-one), Switch (D-link - 24 ports).

## Computer Jr. Lab B - (Total No. of PC-39)

Computer Configuration
Hard Disk 500 GB and 160 GB, RAM 4 GB \& 1 GB, Motherboard Intel \& Dual-core 3.00, Processor (Intel pen 2.90 GHz ), Monitor LED \& TFT 15 inch, Mouse (USB), Keyboard(USB), Dot matrix printer (LQ-1150 II EPSON-one), Switch (D-link - 24 ports (2) ).

## Computer Lab C3

## Digital Electronics

1. Study of Synchronous and Asynchronous Counter using IC (02), DAC - 0800 (02), DAC0808 (02), Study of 4-bit Binary adder and substractor using IC 7483 (02), Study of Logic gates (02), Study of shift register using flip flop (02), Arithmetic logic unit (02), Study of EPROM (02), Multiplexer (8:1) (02), Ring Counter (02), Study of Flip flop (02), Random Access Memory (RAM) demonstrator (02), Synchronous counter (MOD-8 and 16) (02), 4-bit binary adder and substractor (02), Demultiplexer and decoder ( $1: 8$ and $3: 8$ ) Z (02), Asynchronous Up/down counter (MOD 8,10,16)11102 (02), A and D converter (02), Total Digital Kits - (34)

SMPS - 12 V Large [03], SMPS - 12 V Small [02], Banana pins [50], Amplifier 10.IV [08]

SMPS - 12 V Large [03], SMPS - 12 V Small [02], Banana pins [50], Amplifier 10.IV [08]

## Microprocessor and Interfacing

Interfacing of 8 bit 8 LED panel to port ABC of 8255 for binary Up/Down counter [03], Study of 7 segment display without Multiplexing [03], Study of 7 segment display without Multiplexing [03], Interfacing of D/P switches with LED indications for Port ABC [03], Study card for 8255 [03], Study card for 8251 [03], Study card for 8279 [03], Digital to Analog converter (PIO) [02], Analog to Digital converter (PIO) [02], LBDR (PIO) [02], Stepper Motor [02],

Study card for 8253 [02], Study Interfacing of 8 switches and LED panel to display the Status of switches [03], Microprocessor Dynalog Kits 8086 [01], Total Microprocessor and Interfacing [44]
 Ausangabad

| Sr. No. | Name of the Kit | Quantity |
| :--- | :--- | :--- |
| 1 | Study of Synchronous and Asynchronous Counter using IC | 2 |
| 2 | DAC 0800 | 2 |
| 3 | DAC 808 | 2 |
| 4 | Study of 4-bit Binary adder and substractor using IC 7483 | 2 |
| 5 | Study of Logic gates | 2 |
| 6 | Study of shift register using flip flop | 2 |
| 7 | Arithmetic logic unit | 2 |
| 8 | Study of EPROM | 2 |
| 9 | Multiplexer (8:1) | 2 |
| 10 | Ring Counter | 2 |
| 11 | Study of Flip flop | 2 |
| 12 | Random Access Memory (RAM) demonstrator | 2 |
| 13 | Synchronous counter (MOD-8 and 16) | 2 |
| 14 | 4-bit binary adder and substractor | 2 |
| 15 | Demultiplexer and decoder (1:8 and 3:8) | 2 |
| 16 | Asynchronous Up / down counter (MOD 8,10,16) | 2 |
| 17 | A and D converter | 2 |
|  |  | 34 |
|  |  | Total |



PRINCIPAL

